

IN THE CLAIMS:

Please amend claims 23, 26, 32, 34, 37, 38, 41 and 42 as follows:

b2 23. (amended) A liquid crystal display apparatus comprising:

a pair of substrates, at least one of which is transparent;

a liquid crystal layer formed by sandwiching a liquid crystal composition between said pair of substrates;

a display region having a plurality of first semiconductor elements which are arranged in a matrix on one substrate of said pair of substrates;

at least one peripheral circuit having a plurality of second semiconductor elements arranged at a periphery of said display region, said at least one peripheral circuit being formed on said one substrate of said pair of substrates and at least one part of said at least one peripheral circuit being arranged in a peripheral circuit region which is held between said pair of substrates; and

at least one driver circuit which is electrically connected to said at least one peripheral circuit for driving said at least one peripheral circuit being arranged outside of a region which is held between said pair of substrates.

b3 26. (amended) A liquid crystal display apparatus according to claim 23, wherein an amplitude of a liquid crystal driving source voltage of said at least one driver circuit is no greater than about 5V.

b4 32. (amended) A liquid crystal display apparatus comprising:

a pair of substrates, at least one of which is transparent;

a liquid crystal layer formed by sandwiching a liquid crystal composition between said pair of substrates;

a display region having a plurality of semiconductor elements arranged in a matrix on one substrate of said pair of substrates;

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an image signal peripheral circuit which consists of a switch matrix circuit
connected to said display region on one substrate of said pair of substrates; and
at least one driver circuit electrically connected to said image signal peripheral
circuit.

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34. (amended) A liquid crystal display apparatus according to claim 33,
wherein at least one of said image signal peripheral circuit and said scanning signal
peripheral circuit includes a plurality of semiconductor elements having at least one
semiconductor island annealed by laser irradiation.

37. (amended) A liquid crystal display apparatus according to claim 36,
wherein said switch matrix circuit comprises thin-film transistors, and said thin-film
transistors have a mobility in the range of 100 cm²/Vs to 300 cm²/Vs.

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38. (amended) A liquid crystal display apparatus comprising:
a pair of substrates, at least one of which is transparent;
a liquid crystal layer formed by enclosing a liquid crystal composition between
said pair of substrates;
a display region having a plurality of semiconductor elements arranged in a
matrix form on one substrate of said pair of substrates;
at least one image signal peripheral circuit having a switch matrix circuit
connected to said display region; and
at least one driver circuit, including at least one display information generating
circuit, electrically connected to said at least one image signal peripheral circuit.

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41. (amended) A liquid crystal display apparatus according to claim 40,
wherein said excimer laser is a XeCl excimer laser.

42. (amended) A liquid crystal display apparatus comprising:
a pair of substrates, at least one of which is transparent;
a liquid crystal layer formed by sandwiching a liquid crystal composition
between said pair of substrates;
a display region having a plurality of first semiconductor elements arranged in
a matrix form on one substrate of said pair of substrates; and
an image signal peripheral circuit having a switch matrix circuit connected to
said display region;
wherein only one driver circuit is electrically connected to said image signal
peripheral circuit for generating clock pulses and image signals.

Please add the following new claims:

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~~45~~. A liquid crystal display apparatus according to claim 42, wherein said
image signals are analog image signals.

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~~46~~. A liquid crystal display apparatus according to claim 21, wherein the
amplitude of the liquid crystal driving source voltage of said at least one driver circuit
is no greater than about 3V.--

REMARKS

This amendment is submitted in connection with the RCE of the continuation of the reissue application, and is responsive to the final Office Action dated October 8, 2002, a Notice of Appeal being filed April 8, 2003.

By the present amendment, the Cross-Reference to the Related Application has been updated in accordance with the Examiner's requirement, the added reissue claims 23-45 have been amended taking into consideration the points raised by the